What is claimed is:

1. A skew correction circuit comprising:

a first circuit to generate a pulse train signal in response to a data bit signal and a first strobe signal, a duty cycle of the pulse train signal being indicative of a degree of skew between the data signal and the strobe signal; and

a second circuit coupled to the first circuit to produce a second strobe signal and regulate a timing relationship between the data bit and the second strobe signal based on the duty cycle of the pulse train.

2. The skew correction circuit of claim 1, wherein the second circuit comprises:

an error register to store a calibration value indicative of the degree of skew.

3. The skew correction circuit of claim 2, wherein the second circuit further comprises:

a delay chain coupled to the error register to delay the first strobe signal based on the calibration value to produce the second strobe signal.

4. The skew correction circuit of claim 1, wherein said at least one pulse train signal comprises:

a first pulse train signal having a duty cycle that increases with an increase in the degree of skew and a second pulse signal having a duty cycle that decreases with a decrease in the degree of skew.

5. The skew correction circuit of claim 4, wherein the first circuit further comprises:

a first low pass filter to filter the first pulse train signal to produce a first filtered signal;

a second low pass filter to filter the second pulse train signal to produce a second filtered signal; and

an amplifier to produce the indication of the degree of skew based on the difference of the first and second filtered signals.

6. The skew correction circuit of claim 1, wherein the duty cycle of said at least one pulse train signal indicates the degree of skew.

7. A data receiver comprising:

buffers, each buffer to latch a different data bit signal;

a first circuit to generate at least one pulse train signal in response to a strobe signal and for each data bit signal, a duty cycle of said at least one pulse train signal indicating a degree of skew between the data bit signal and the strobe signal; and

a second circuit coupled to the first circuit and the buffers to regulate latching of the data bit signals by the buffers based on the indicated degrees of skew.

8. The data receiver of claim 7, wherein the first circuit comprises:

registers, each register being associated with a different one of the data bit signals and indicating the degree of skew between the strobe signal and the associated data bit signal.

9. The data receiver of claim 7, wherein the second circuit comprises:

a delay chain to receive the strobe signal, the delay chain including taps indicating the strobe signal delayed by different delays; and

multiplexing circuitry to selectively couple the taps to the buffers based on the indicated degrees of skew.

10. The data receiver of claim 7, further comprising:

multiplexing circuitry to select one of the data bit signals, and

wherein the first circuit comprises a third circuit to provide said at least one pulse train signal indicative of the degree of skew between the selected data bit signal and the strobe signal.

11. The data receiver of claim 10, wherein said at least one pulse train signal comprises:

a first pulse train signal having a duty cycle that increases with an increase in the degree of skew between the selected data bit signal and the strobe signal and a second pulse signal having a duty cycle that decreases with a decrease in the degree of skew between the selected data bit signal and the strobe signal.

12. The data receiver of claim 11, wherein the first circuit further comprises:

a first low pass filter to filter the first pulse train signal to produce a first filtered signal;

a second low pass filter to filter the second pulse train signal to produce a second filtered signal; and

an amplifier to produce the indication of the degree of skew between the selected data bit signal and the strobe signal based on the difference of the first and second filtered signals.

A method comprising:

using a data bit signal and a first strobe signal to generate pulse train signal, a duty cycle of the pulse train signal indicating a degree of skew between the data bit signal and the strobe signal; and

regulating a timing relationship between the data bit and the second strobe signal based on the degree of skew indicated by the duty cycle.

14. The method of claim 13, wherein the indicating comprises:

storing a calibration value indicative of the degree of skew.

15. The method of claim 14, further comprising:

delaying the first strobe signal based on the calibration value to produce the second strobe signal.

16. The method of claim 13, wherein said at least one pulse train signal comprises:

a first pulse train signal having a duty cycle that increases with an increase in the degree of skew and a second pulse signal having a duty cycle that decreases with a decrease in the degree of skew.

17. The method of claim 16, further comprising:

filtering the first pulse train signal to produce a first filtered signal;

filtering the second pulse train signal to produce a second filtered signal; and

amplifying a difference of the first and second filtered signals to indicate the degree of skew.

18. The method of claim 13, wherein the duty cycle of said at least one pulse train signal indicates the degree of skew.

19. The method of claim 13, further comprising:

causing the data bit signals to indicate a predetermined data pattern to generate the pulse train signal.